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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10047789	FILING DATE 01/15/2002	CLASS 714	SUBCLASS 718	GAU 2133	EXAMINER J-KERUEFOI
**APPLICANTS: Park Jin-Young;					
**CONTINUING DATA VERIFIED: <i>none</i> <i>ju 5/5/04</i>					
** FOREIGN APPLICATIONS VERIFIED: <i>yes</i> <i>ju 5/5/04</i> REPUBLIC OF KOREA 2001-34736 06/19/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>			RESCIND <input type="checkbox"/>		
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials			ATTORNEY DOCKET NO 8836-147 (IB11125-US)		
TITLE : Built-in self test circuit using linear feedback shift register					

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NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Shoots Drwg.	Figs.Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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